



Processor Architecture: From Dataflow To Superscalar And Beyond

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Jurij Silc

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Processor Architecture Offers a survey of architectural mechanisms and implementation techniques for exploiting fine- and coarse-grained parallelism within microprocessors. Beginning with a review of past techniques, this monograph provides an account of techniques used in microprocessors, covering both the concepts involved and implementations in sample processors. Full description

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389 pages

Présentation de l'éditeur

This monograph surveys architectural mechanisms and implementation techniques for exploiting fine-grained and coarse-grained parallelism within microprocessors. It starts with a review of past techniques, continues with a comprehensive account of state-of-the-art techniques used in microprocessors that covers both the concepts involved and implementations in sample processors, and ends with a thorough review of the research techniques that will lead to future microprocessors.

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